

Please substitute the following abstract for the abstract currently pending.

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**Substitute Abstract**

B2 A receiver includes a processor and an RF bridge. The RF bridge is coupled to the processor to receive a reference signal from the processor. The RF bridge includes first and second frequency converters coupled to respective first and second antennas. The RF bridge also includes a third frequency converter coupled to outputs of the first and second frequency converters. In an alternative receiver the processor is coupled to receive an information signal from the RF bridge. The processor includes a digital frequency source to generate the reference signal based on a signal from a clock source and circuitry to detect a frequency difference from the information signal based on the signal from the clock source. In a variant, the circuitry to detect includes a first Fourier transformer having a first center frequency and a second Fourier transformer having a second center frequency different than the first center frequency.

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